

# A Structured SADT Approach to the Support of a Parallel Adaptive 3D CFD Code

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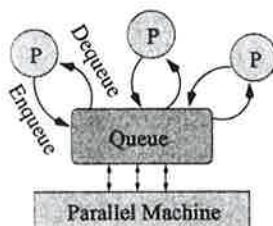
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**Abstract.** The parallel implementation of unstructured adaptive tetrahedral meshes for the solution of transient flows requires many complex stages of communication. This is due to the irregular data sets and their dynamically changing distribution. This paper describes the use of Shared Abstract Data Types (SADTs) in the restructuring of such a code, called PTETRAD. SADTs are an extension of an ADT with the notion of concurrent access. The potential for increased performance and simplicity of code is demonstrated, while maintaining software portability. It is shown how SADTs can raise the programmer's level of abstraction away from the details of how data sharing is supported. Performance results are provided for the SGI Origin2000 and the Cray T3E machines.

## 1 Introduction

Parallel computing still suffers from a lack of structured support for the design and analysis of code for distributed memory applications. For example, the MPI library supports a portable set of routines, such that applications can be more readily moved between platforms. However, MPI requires the programmer to become involved in the detailed communication and synchronisation patterns which the application will generate. The resulting code is hard to maintain, and it is often difficult to determine which code segments might require further attention in order to improve performance. In addition, a portable interface does not imply portable performance - different MPI codes may have to be written to obtain good performance on new platforms.

Abstract Data Types (ADTs) have been used in serial computing to support modular and re-usable code. An example is a Queue, supporting a well-defined interface (Enqueue and Dequeue methods) which separates the functionality of the Queue from its internal implementation.



Whereas an ADT supports information sharing between the different components of an application, a Shared ADT (SADT) [6, 1, 3] can support sharing between applications executing across multiple processors. High performance in a parallel environment is supported by allowing the concurrent invocation of the SADT methods, where multiple Enqueue and Dequeue operations can be active across the processors.

The clear distinction between functionality and implementation leads to portable application code, and portable performance, since alternative SADT implementations can be examined without altering the application. The potential to generate re-usable SADTs means that greater degrees of investment, care and optimisation can be made in the implementation of an SADT on a given platform. For example, the implementation of a Queue on the Cray T3D [5] can support an increase in performance of 110, when the number of processors increase by a factor of 128. This was achieved by providing very high levels of fine-grain concurrency within the SADT implementation. In contrast, a more typical (lock-based) implementation has a performance ceiling of around 20.

In addition, an SADT can be parameterised by one or more user serial functions, in order to tailor the functionality of the SADT to that required by the application. For example, a shared Accumulator SADT [2] can produce a result based on the combined inputs supplied by each processor. A user function can be supplied which then determines the format of the inputs, and how those inputs are combined. The simplest case may be to sum an integer value at each processor. A more complex example is for each processor to submit a vector, and for the combining action to sum only the positive elements selected from each vector. This user parameterised form of the SADT is particularly useful in dealing with different parts of complex data structures in different ways.

This paper describes work <sup>1</sup> investigating the use of SADTs in a parallel computational fluid dynamics code, called PTETRAD [7, 8, 9]. The unstructured 3D tetrahedral mesh, which forms the basis for a finite volume analysis, is partitioned among the processors by PTETRAD. Mesh adaptivity is performed by recursively refining and de-refining mesh elements, resulting in a local tree data structure rooted at each of the original base elements. The initial mesh partitioning is carried out at this base element level, as is any repartitioning and redistribution of the mesh when load imbalance is detected.

A highly interconnected mesh data structure is used by PTETRAD, in order to support a wide variety of solvers and to reduce the complexity of using unstructured meshes. Nodes hold a one-way linked list of element pointers. Nodes and faces are stored as a two-way link list. Edges are held as a series of two-way linked lists (one per refinement level) with child and parent pointers. In addition, frequently used remote mesh objects are stored locally as halo copies, in order to reduce the communications overhead. The solver, adaptation and redistribution phases each require many different forms of communication within a parallel machine in order to support mesh consistency (of the solution values and the data structures), both of the local partition of the mesh and the halos. PTETRAD currently uses MPI to support this.

In this paper it will be shown how parts of PTETRAD may be used in SADTs based on top of MPI and SHMEM, instead of MPI directly, thus leading to software at a higher level of abstraction with a clear distinction between the serial and parallel parts of the code. Section 2 describes an SADT which has been designed to support the different mesh consistency protocols within an

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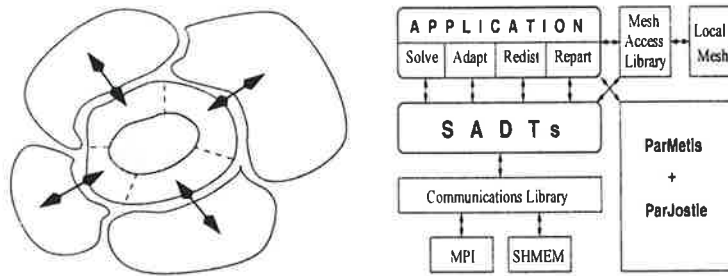


Fig. 1. (a) The SADT update method; (b) The new PTETRAD structure

unstructured tetrahedral mesh. A case study in Section 3 will describe the use of the SADT in supporting the mesh redistribution phase. A brief overview of the implementation techniques for the SADT will be given in Section 4, together with performance results for the SADT and for PTETRAD. The paper concludes by pointing to some current and future work.

## 2 An SADT for Maintaining Data Partition Consistency

The SADT described in this paper has focused on the problem depicted in Figure 1(a). A data set has been partitioned among  $p$  processors, with each processor holding internal data (the shared area), and overlapping data areas which must be maintained in a consistent state after being updated. PTETRAD maintains an array of pointers to base and leaf elements, which can be used to determine the appropriate information to be sent between partitions. For example, after mesh adaptation, any refined elements will require that their halo copies also be refined. Also, in the redistribution phase, the base element list can be used to determine which elements need to be moved between partitions. In constructing an SADT for this pattern of sharing, four basic phases of execution can be identified. The SADT contains a consistency protocol which specifies these phases of execution, with the generic form:

```
void Protocol (in, out) /* Protocol interface with input... */
{
    /* and output data lists. */
    int send[p], recv[p]; /* Counters used in communications. */
    pre-processing; /* Initialisation of internal data. */
    communications preamble; /* Identifying how much data will... */
    /* be exchanged between partitions. */
    data communications; /* Exchanging and processing the... */
    /* new data between the partitions. */
    post-processing; /* Format the results. */
}
```

The protocol is called with a set of user-supplied input and output data lists (*in* and *out*), for example the lists of element pointers described above,

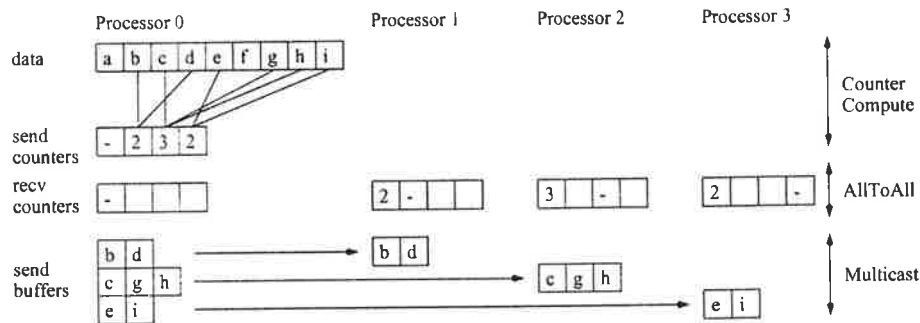


Fig. 2. The SADT communications stages

and each phase requires the user to supply a number of application-specific serial functions. The SADT is thus parameterised by these user functions which allow the communications phases to be tuned by evaluating various condition functions, for example to determine whether a data item is to be communicated to a given partition (if it has been refined or needs to be redistributed). The SADT also contains basic functions to pack/unpack selected fields of data items to/from message buffers, and to process the new data items which are received.

Figure 2 shows an example of the operation of the protocol. All processors will be performing the same phases of execution, but only the operation of processor 0 is shown here, for reasons of clarity (the pre-processing and post-processing phases are also removed). The protocol can make use of a communications library for global communications operations (denoted by `Comms_Function`), and may require one or more user-defined serial functions (denoted by `User_Function`).

- (i) The communications preamble is given by:
  - (a) `Comms_CounterCompute`: For each data item, `User_CounterCondition` decides if it is to be communicated, and `User_CounterIndexing` will update the associated values in the counters `send[]` and `recv[]`.
  - (b) `Comms_AllToAll`: An all-to-all communication is executed, in which the other processors note the expected number of items to be received from processor  $i$  in `recv[i]` (if `Comms_CounterCompute` is able to determine the counter values in `recv[]` then this communication can be avoided).
- (ii) The actual data communications phase is given by `Comms_Multicast`:
  - (a) For each input item and each processor in turn, `User_SendCondition` decides if the item should be sent to the processor. `User_PackDatum` will choose the selected fields of the data item to send, and place them in a contiguous memory block, so that it can be copied into the message buffer for that processor (`User_DatumSize` allows the system to allocate the required total send and receive buffer space).
  - (b) Once the buffers have been communicated between the processors, each item is removed in turn, using `User_UnpackDatum`, and the local data partition is updated, based on this item, with `User_ProcessDatum`.

### 3 Case Study: Mesh Redistribution

The new structure of PTETRAD [9] is shown in Figure 1(b). At the application level, local mesh access is supported by a library of mesh routines. The mesh repartitioning strategy is handled by linking in parallel versions of either the Metis or Jostle packages. The global mesh consistency is handled by making calls to the SADT, which also performs local mesh updates through the mesh library. The coordination between processors is supported by a small communications library which supports common traffic patterns, from a simple all-to-all exchange of integer values, up to the more complex packing, unpacking and processing of data buffers which are sent to all neighbouring mesh partitions.

The new SADT-based approach makes use of MPI, so that it may be run on both massively parallel machines and on networks of workstations, and also uses the Cray/SGI SHMEM library, to exploit the high performance direct memory access routines present on the SGI Origin 2000 and the Cray T3D/E. The use of an alternative communications mechanism is simply a matter of writing a new communications library (typically around 200 lines of code), and linking the compiled library into the main code.

The operation of the redistribution phase can be divided into four stages:

- **Repartition:** Compute the new mesh partitions at the base element level, using the parallel versions of Metis or Jostle.
- **Assign owners:** Update the local and halo owner fields for elements, edges, nodes and faces.
- **Redistribute:** Communicate the data to be moved and the new halo data.
- **Establish links:** Destroy any old communications links between local and halo mesh objects, and create the new links.

The following examples focus on the second stage of assigning the new owners for edges in the partitioned mesh, in which the halo edges must be updated with the new owner identifiers. PTETRAD maintains an array of edge lists, with each list holding the edges at a given level of refinement in the mesh. An edge stores pointers to the halo copies which reside on other processors. This array is used as the input to the SADT protocol, with the user functions processing each edge list in turn.

#### 3.1 The counter-related SADT functions

Referring back to the SADT protocol stages in Section 2, Figure 3 describes the main user-supplied function required for the `Comms_CounterCompute` procedure. This relates to the computation of the send counter values, which describe the amount of actual data which will follow. For reasons of clarity, the other functions have been omitted here.

`User_CounterIndexing` will take the given edge list, and traverse the list of halos associated with each edge. The identifier of the processor holding each halo item, given by  $halo \rightarrow proc$ , will result in the increment of the associated send counter. This supports the computation of the counters within



### Update the counters

```
void User_CounterIndexing (
PTETRAD_Edge *edge,      /* a list of mesh edges */
int *send, *recv)       /* the SADT data counters */
{
    PTETRAD_EdLnk *halo; /* edge halo pointer */
    while (edge) {       /* inspect each edge */
        halo = edge → halo; /* inspect the halos of the edge */
        while (halo) {   /* for each edge halo */
            send[halo → proc]++; /* the halo resides on processor halo → proc */
            halo = halo → next; /* go on to the next halo */
        }
        edge = edge → next; /* go on to the next edge */
    }
}
```

Fig. 3. SADT counter-related user function

**Comms\_CounterCompute.** The **Comms\_AllToAll** call then initiates the communication of the counters. At this stage, each processor now has access to the amount of data which it will be receiving from the other processors, and the amount that it will send to them.

### 3.2 The data transmission SADT functions

Figures 4 and 5 provides a description of the user functions required for the **Comms\_Multicast** procedure in the SADT consistency protocol. This relates to the packing, communication, unpacking and processing of the actual mesh edges. Once again, only the key user functions are given, for reasons of clarity.

Figure 4 shows the initial packing stage. **User\_DatumSize** returns the size of the “flattened” data structure, which forms the contiguous memory area to be communicated. In this case, it is an address of a halo edge on a remote processor, and the new owner identifier to be assigned to it. These details are held in the variable *Comm*. For a particular processor, **User\_PackDatum** is used to search a list of edges at a given mesh refinement level, and determine if any edge halos are located on that processor. Those halos are packed into a contiguous buffer area, ready for communication. The “Pack” function is a standard call within the SADT library, which will copy the data into the communication buffer. At this stage, the data buffers have been filled, and **Comms\_Multicast** will carry out the communication between the processors.

### 3.3 The data reception SADT functions

Figure 5 shows the unpacking and processing stage, once the data has been exchanged. **User\_UnpackDatum** will transfer the next data block from the communications buffer into the *EdgeOwner* variable. **User\_ProcessDatum** will use the *ed* field to access the halo edge, and set its owner identifier to the new value.

#### Data type for communication

```
struct Comm_type {
    PTETRAD_Edge *ed;
    int own;
} Comm;
```

#### The size of the data type

```
int User_DatumSize ()
{
    return (sizeof(struct Comm_type));
}
```

#### Pack the datum into a buffer

```
void User_PackDatum (
    PTETRAD_Edge *edge, /* a list of mesh edges */
    char *buf, int *pos, /* storage space is available at buf[*pos] */
    int pe) /* inspect all halo edges located on processor pe */
{
    PTETRAD_EdLnk *halo; /* edge halo pointer */
    int size = User_DatumSize(); /* the amount of storage required */
    while (edge) { /* inspect each edge */
        halo = edge → halo; /* inspect the halos of the edge */
        while (halo) { /* for each edge halo */
            if (Edge_HaloHome (halo, pe)) { /* is the halo on processor pe ? */
                /* PACK THE HALO */
                Comm.ed = halo → edge; /* note the halo's local address... */
                Comm.own = edge → owner; /* on processor pe, and the owner */
                Pack (&Comm, size, buf, pos); /* pack this into the buffer area */
            }
            halo = halo → next; /* go on to the next halo */
        }
        edge = edge → next; /* go on to the next edge */
    }
}
```

Fig. 4. SADT data communication functions: sending side

### 3.4 Comments

The use of SADTs to support the consistency of distributed mesh data has a number of advantages. The programmer is no longer concerned with how the communication of the data buffers takes place; a set of serial functions need only be defined in order to specify the particular mesh consistency procedure. In restructuring the redistribution phase of PTETRAD, the amount of code has also been substantially reduced (see Section 4.1). Additional reductions in code are available when SADT templates are used (see Section 5), which enable further simplification of the user functions. Since all sharing is carried out through the SADTs, the communications harness can be readily changed, without altering the application code (see the next section).

Unpack the datum from a buffer	Update the local mesh partition
<pre>void User_UnpackDatum ( void *in, char *buf, int *pos) {     Unpack (&amp;Comm,             User_DatumSize(), buf, pos); }</pre>	<pre>void User_ProcessDatum ( void *in, void *out) {     (Comm.ed) → owner =     Comm.own; }</pre>

Fig. 5. SADT data communication functions: receiving side

Original PTETRAD version:

	Repartition + assign owners	Redistribute + establish links	Total
Appl.	1,780 / 53	7,300 / 216	9,080/269

SADT PTETRAD version:

	Repartition + assign owners	Redistribute + establish links	Total
Appl.	230 / 8	300 / 9	530/17
SADTs	440 / 11	1,660 / 40	2,100/51
Mesh			2,590/74
		TOTAL:	5,220/142

SADT libraries:

Update SADT	200 / 6
Exchange SADT	25 / 1
MPI Library	220 / 6
SHMEM Library	170 / 5
TOTAL	615 / 18

Table 1. A summary of the source code requirements (lines / KBytes)

## 4 Implementation Details and Performance Results

### 4.1 A summary of the source code requirements

Table 1 summarises the amount of source code in the original and new PTETRAD versions, for the mesh redistribution phase. The amount of code which the programmer must write has been reduced from 9,080 to 5,220 lines. A drastic reduction in the amount of application code has been achieved by supporting the stages of global mesh consistency as SADT calls, and implementing the mesh access operations within a separate library. The mesh access library is also being re-used during the restructuring of the solver and adaption phases. As a typical example, the code for the communication of mesh nodes during redistribution is reduced from 340 lines to 100 lines, with only around 20 of these lines performing actual computation.

Within the SADT library, the main *Update* SADT, for maintaining mesh consistency, contains 200 lines of code, and an *Exchange* SADT (for performing gather/scatter operations) contains 25 lines. The MPI and SHMEM communications harnesses each have their own library which support the *Comms\_Function* operations (see Section 2 and below). New libraries can easily be written to exploit new high performance communications mechanisms, without any changes to the application code.



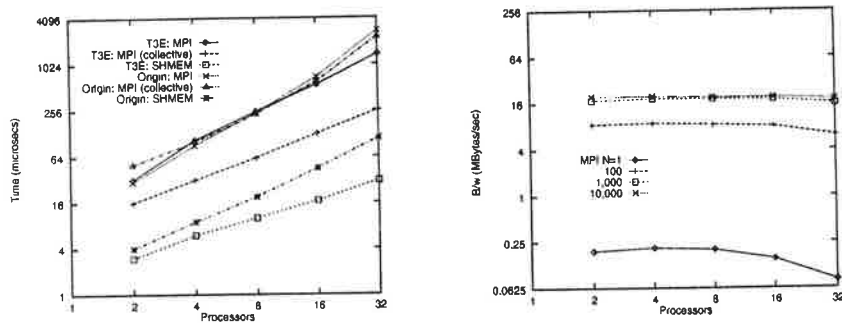


Fig. 6. (a) Comms\_AllToAll; (b) Comms\_Multicast: MPI on Origin 2000

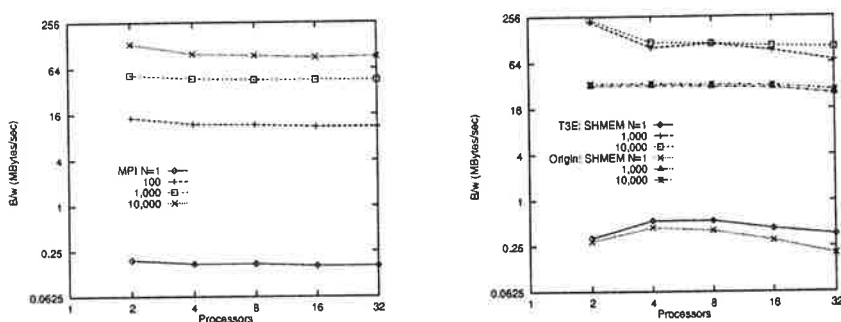


Fig. 7. Comms\_Multicast: (a) MPI on Cray T3E; (b) SHMEM on both machines

Origin 2000:

	Adaption	Imbalance	Repartition	Redistribute	Imbalance	Solve
PTETRAD (4)	7.00	30 %	1.55	<b>3.29</b>	11 %	1.60
SADT-MPI (4)	6.97	30 %	1.52	<b>3.03</b>	11 %	1.60
SADT-COLL (4)	8.01	30 %	1.52	<b>3.04</b>	11 %	1.60
SADT-SHMEM (4)	6.96	30 %	1.53	<b>3.01</b>	11 %	1.60
PTETRAD (32)	3.01	26 %	0.49	<b>1.74</b>	13 %	0.20
SADT-MPI (32)	3.04	26 %	0.48	<b>1.72</b>	13 %	0.20
SADT-COLL (32)	3.02	26 %	0.47	<b>1.74</b>	13 %	0.20
SADT-SHMEM (32)	3.02	26 %	0.47	<b>1.73</b>	13 %	0.20

Cray T3E:

	Adaption	Imbalance	Repartition	Redistribute	Imbalance	Solve
PTETRAD (4)	5.87	19 %	0.99	<b>3.26</b>	11 %	1.23
SADT-MPI (4)	5.88	19 %	0.98	<b>3.04</b>	11 %	1.23
SADT-COLL (4)	5.89	19 %	0.98	<b>3.10</b>	11 %	1.23
SADT-SHMEM (4)	5.86	19 %	0.97	<b>2.78</b>	11 %	1.23
PTETRAD (32)	4.40	26 %	0.38	<b>2.12</b>	11 %	0.20
SADT-MPI (32)	4.38	26 %	0.38	<b>1.92</b>	11 %	0.20
SADT-COLL (32)	4.45	26 %	0.37	<b>1.92</b>	11 %	0.20
SADT-SHMEM (32)	4.44	26 %	0.37	<b>1.96</b>	11 %	0.20

Table 2. PTETRAD gas dynamics performance results (timings in seconds)

## 4.2 The SADT communications library

The communications operations employed by an SADT are supported by a small communications library, as outlined in Section 2 and above. This contains operations such as an all-to-all exchange (`Comms_AllToAll`), and the point-to-point exchange and processing of data buffers representing new or updated mesh data (`Comms_Multicast`).

Figure 6(a) shows the performance of `Comms_AllToAll` for the platforms being studied. On the Origin, the difference between using MPI send-receive pairs and the collective routine `MPI_Alltoall` is quite small. Pairwise communication performs better up to around 8 processors. Collective communications take 2,300  $\mu\text{secs}$  on 32 processors, as opposed to 2,770  $\mu\text{secs}$  for the pairwise implementation. On the T3E, pairwise communication performs very similarly, but the collective communications version performs quite substantially better in all cases (eg 258  $\mu\text{secs}$  down from 1377  $\mu\text{secs}$  on 32 processors), outperforming the Origin version. For both platforms, it can be seen that the pairwise implementation begins to increase greater than linearly, due to the  $p^2$  traffic requirement, whereas the collective communications version stays approximately linear. A third implementation, using the SHMEM library, outperforms pairwise communication by at least an order of magnitude, due to its very low overheads at the sending and receiving sides, taking 30  $\mu\text{secs}$  on 32 processors for the T3E, and 110  $\mu\text{secs}$  on the Origin. In PTETRAD, this stage of communication represents a small fraction of the overall communications phase, so it is not envisaged that alternative implementation approaches will have any real impact on performance.

Figures 6(b) and 7 show the performance of `Comms_Multicast`, in which each processor  $i$  exchanges  $N$  words with its four neighbours  $i-2$ ,  $i-1$ ,  $i+1$  and  $i+2$  (in the case of  $p \leq 4$ , exchange occurs between the  $p-1$  neighbours) (the user (un)packing and processing routines are null operations). On the Origin, performance reaches a ceiling of around 20 MBytes/sec for  $N = 1000$  or larger using MPI, and 33 MBytes/sec using SHMEM, across the range of processors. For very small messages, the overheads of MPI begin to have an impact as the number of processors increase. On the T3E, the achievable performance using MPI was significantly higher, supporting 88 MBytes/sec on 32 processors, for large messages. Using SHMEM, this increases to 103 MBytes/sec, as well as improving the performance for smaller messages. Since this benchmark is measuring the time for all processors to both send and receive data blocks, the bandwidth results can be approximately doubled in order to derive the available bandwidth per processor. PTETRAD typically communicates messages of size 10K – 100K words, by using data blocking, and the above results show that this should make effective use of the available communications bandwidth.

## 4.3 PTETRAD performance results

A number of small test runs were performed using the original version of PTETRAD, and the SADT version of the redistribution phase, using pairwise MPI, collective MPI communication and SHMEM. A more comprehensive description

of the performance of PTETRAD can be found in [8, 9]. Table 2 shows some typical results on the Origin and T3E, for a gas dynamics problem described in [9], using 4 and 32 processors.

The results for the Origin show an 8% reduction in redistribution times on 4 processors, and 4% for 32 processors. The use of the SHMEM library doesn't improve performance any further in this case, since the high level of mesh imbalance mean that local computation is the dominant factor. Thus, the performance improvements when using the SADT approach originate from the tuning of the serial code. The other timings are approximately equal, pointing to the fact that the improved redistribution times are real, rather than due to any variation in machine loading. The T3E results show a reduction in times of between 7% and 10% using MPI, and a reduction of 15% on 4 processors by linking in the SHMEM communications library. The lower initial mesh imbalance, coupled with the very high bandwidths available using SHMEM, result in this significant performance increase. The slight increase in time on 32 processors using SHMEM seems to be due to a conflict between SHMEM and MPI on the T3E. When the complete PTETRAD code is restructured using SADTs, it is envisaged that this conflict will be removed.

The results show how performance can be improved using three complementary approaches. The use of an existing communications library, such as MPI, can be examined, to determine if alternative operations can be used, such as collective communications. Different communications libraries, such as SHMEM, can also be linked in. Finally, due to the clear distinction between the parallel communications and local computation, the serial code executing on each processor can also be more readily tuned. In the case of PTETRAD, the routines to determine the mesh data to redistribute were updated, to reduce the amount of searching of the local mesh partition. This shows up in the performance results by an immediate increase in performance when moving to the SADT version which still uses the MPI pairwise communications.

## 5 Conclusions and Future Work

This paper has described the use of shared abstract data types (SADTs) to structure parallel applications. ~~This approach leads to a number of advantages:~~

- The resulting software is at a higher level of abstraction than, for example, message passing interfaces, since all explicit data sharing and synchronisation is encapsulated within an SADT.
- The clear distinction between the serial and parallel parts of the code allows greater scope for both the optimisation of the local computations and the performance tuning of an SADT on specific platforms.

In the case of the restructuring of the PTETRAD parallel CFD code described in this paper, some of the local data access methods were optimised, providing increased performance. In addition, the use of the Cray/SGI SHMEM communications library has allowed high performance SADT implementations on the Cray T3E platform. The amount of code has also been significantly reduced,

since the SADT used to support mesh consistency can be re-used in many parts of the code.

Currently, the mesh redistribution phase has been completed, with the solver and adaptation stages due for completion in the near future. The redistribution phase has also made use of SADT *templates*, for further simplification. A template specifies many of the details of the `User_Function` operations, described in Section 2, given some assumptions about the input data set to be processed. In the case of the redistribution phase, the actual data to be redistributed is held as an array of processor identifier and local address pointer pairs, rather than having to traverse the mesh to determine the data. These increasingly higher levels of abstraction are aimed at eventually supporting the proposed SOPHIA applications interface [8, 9], which provides an abstract view of a mesh and its halo data, based around the bulk synchronous approach to parallelism [4].

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